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TITLE: System and method for prefetching data to multiple levels of cache including
selectively using a software hint to override a hardware prefetch mechanism

CLAIMS:

3. The system as recited in claim 1, wherein the hardware prefetch engine, in
response to the execution of the single instruction, concurrently prefetches a cache
line into the L1 cache, one or more cache lines into the L2 cache, and one or more
cache lines into the L3 cache.